Application No. 09/811,528
Amendment dated: December 7, 2004

Amendments to the Specification:

Please replace the paragraph beginning at page 4, line 10, which starts with "Generally the present invention..." with the following replacement paragraph.

Generally, the present invention provides a method and system for recovering and generating a clock signal from recovered timestamps in a data stream. Figure 1 illustrates an exemplary embodiment of the system of the present invention. The system **50** comprises a data source **52**, which can be a video or other real time data source, or a static data source, connected to a transmitter **54**. The transmitter **54** forms a synchronous link with the receiver **66**. Regardless of the type of data provided by the data source **52** a synchronous link is provided. The transmitter **54** comprises an encoder **56**, which receives the data from the data source **52**, and prepares it for transmission. The data is then given a timestamp to denote either the encoding time or the transmission time. The timestamp is applied to the encoded data by the time stamp generator **58**. The time stamp generator **58**, is operatively connected to a clock **60**, which supplies the time with which to encode the data. The time stamped data is then provided to a transmitter interface **62**. The elements of the transmitter **54** are known to the art and can take any known configuration without changing the scope of the present invention. The transmitter **54** provides the timestamp encoded data to a transmission channel **64**, which is depicted here as a satellite link, but could be any transmission medium. The transmission medium **64** transfers the data to the receiver **66**. The receiver **66** is comprised of a receiver interface **68**, which accepts the data stream from the transmission medium **64**, and a decoder **138**, which is operatively connected to a clock **144**. The decoder **138** is described in further detail in descriptions of other figures, but includes a clock controller ~~146~~ and a timestamp extractor ~~140~~ (neither shown but later referred to as **146** and **140** respectively). The decoder **138** decodes the received data stream and provides a decoded data stream **70**.